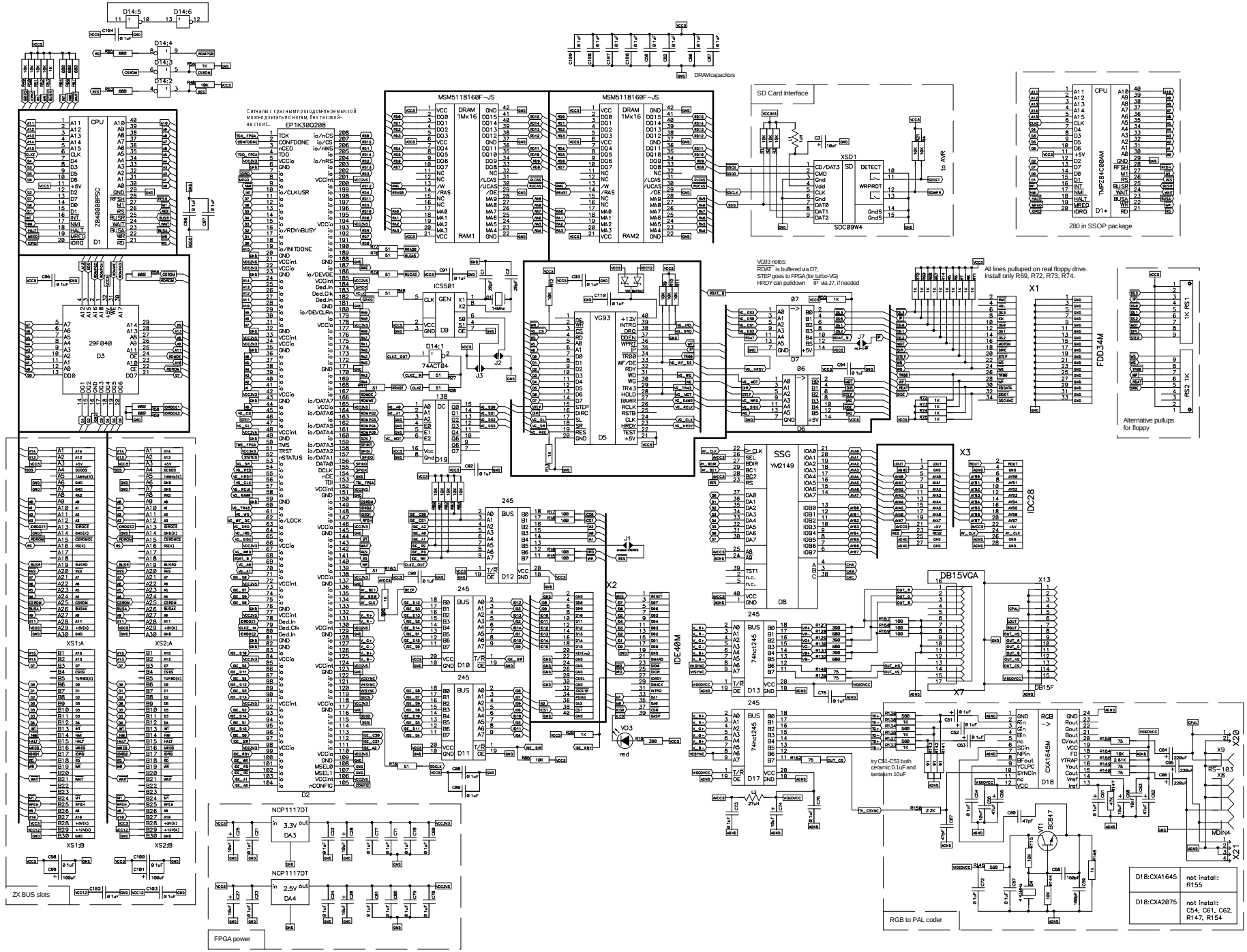
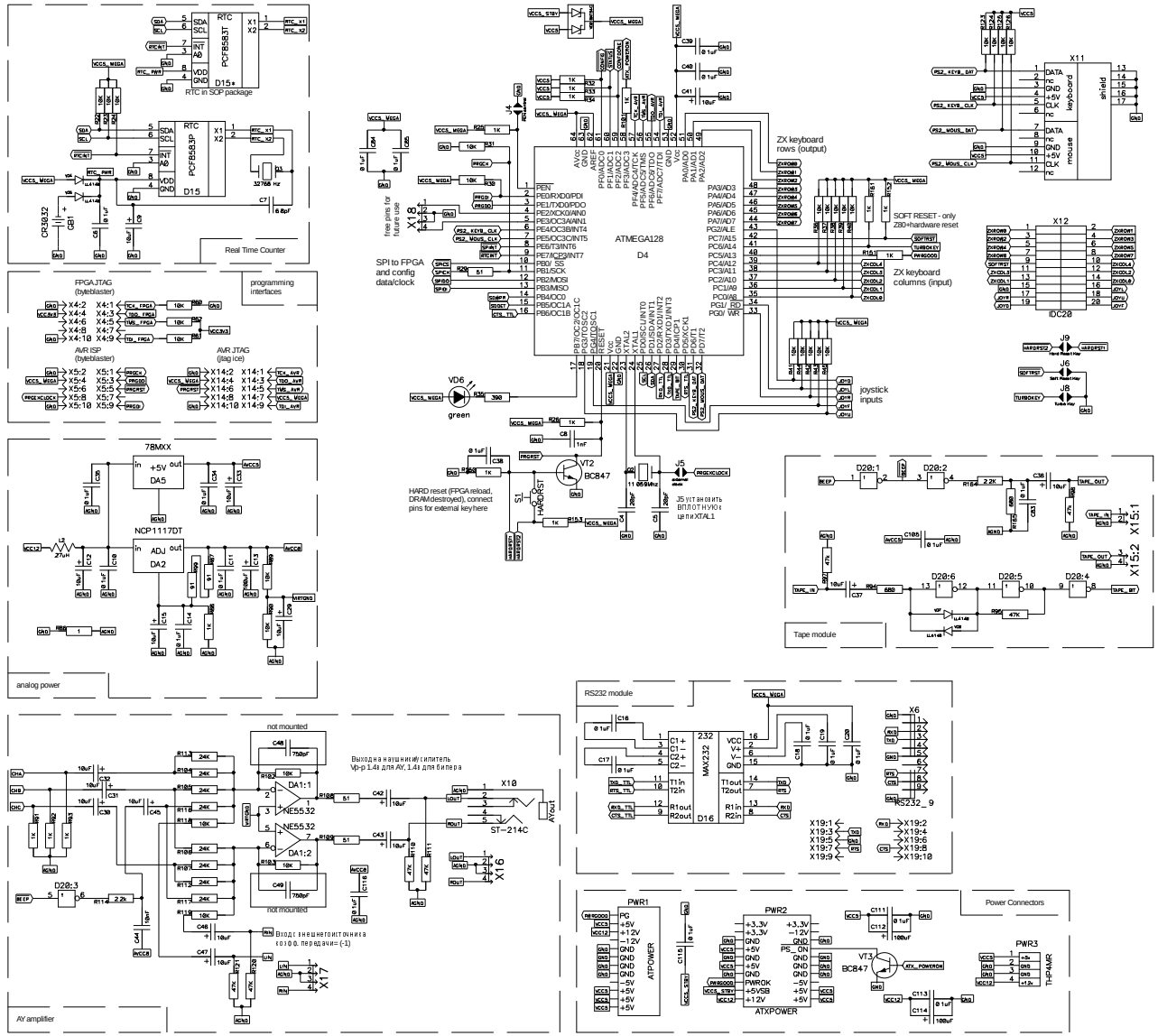

D14:5
D14:6
D14:4
D14:3
D14:2
Z84C0008FSC
CPU
D1
29F040
D3
EP1K30QC208
io/nCS
io/nCSO
io/nRS
io/CLKUSR
io/RDYnBUSY
io/INITDONE
io/DEVOE
io/DEVCLRn
io/DATA7
io/DATA6
io/DATA5
io/DATA4
io/DATA3
io/DATA2
io/DATA1
io/DATA0
io/LOCK
TCK
CONFDONE
nCSO
TDO
VCCIO
GND
TMS
TRST
nSTATUS
DCLK
nCE
TDI
VCCINT
Ded.In
Ded.Clk
nCONFIG
D2
MSM5118160F-JS
DRAM
1Mx16
RAM1
RAM2
DRAM capacitors
SD Card Interface
XSD1
SDC09W4
to AVR
TMP28C208AM
CPU
D1*
Z80 in SSOP package
ICS501
GEN
D9
14MHz
74ACT04
D14:1
D19
VG93
D5
VG93 notes:
RDAT is buffered via D7;
STEP goes to FPGA (for turbo-VG)
HRDY can pulldown IP via J7, if needed
All lines pulluped on real floppy drive.
Install only R69, R72, R73, R74.
D7
D6
J7
X1
FDD34M
1K RS1
RS2 1K
Alternative pullups for floppy
SSG
YM2149
D8
X3
IDC28
DB15VGA
X13
DB15F
245
BUS
D12
D10
D11
D13
D17
74hct245
X2
IDE40M
J1
VD3
red
X7
RGB
CXA1645M
D18
X20
X9
RS-10.3
X8
MDIN4
X21
VT1
BC847
RGB to PAL coder
try C51-C53 both ceramic 0.1uF and tantalum 10uF
XS1:A
XS2:A
XS1:B
XS2:B
ZX BUS slots
NCP1117DT
DA3
3.3V
DA4
2.5V
FPGA power
D18:CXA1645
not install:
R155
D18:CXA2075
not install:
C54, C61, C62,
R147, R154


RTC
PCF8583T
D15e
RTC in SOP package
PCF8583P
D15
32768 Hz
CR3032
GB1
Real Time Counter
FPGA JTAG (byteblaster)
programming interfaces
AVR ISP (byteblaster)
AVR JTAG (jtag ice)
7805XX
+5V
DA5
NCP1117DT
ADJ
DA2
analog power
not mounted
DA1:1
NE5532
DA1:2
D20:3
Выходная наушниковая усилитель
Vp-p 1.4В для AV, 1.4В для бипера
X10
ST-214C
X16
Вход: внешнего источника
питания, передача (-1)
X17
AV amplifier
ATMEGA128
D4
free pins for future use
X18
SPI to FPGA and config data/clock
ZX keyboard rows (output)
ZX keyboard columns (input)
SOFT RESET - only Z80 hardware reset
X11
keyboard
mouse
shield
X12
IDC20
J9
J6
J8
joystick inputs
VD6
green
HARD reset (FPGA reload, DRAM destroyed), connect pins for external keyhere
VT2
BC847
J5
J5 устанавить ВПЛОТНУЮ к пинам XTAL1
Tape module
D20:1
D20:2
D20:6
D20:5
D20:4
X15:1
X15:2
RS232 module
MAX232
D16
X6
RS232_9
PWR1
ATPOWER
PWR2
ATXPOWER
Power Connectors
PWR3
THPAMR